

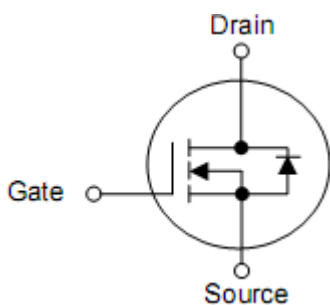
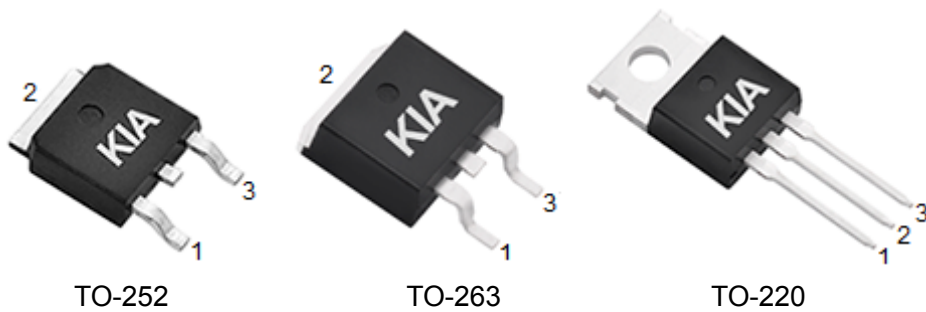
## 1. Features

- $R_{DS(ON)}=2.5m\Omega(\text{typ.})@V_{GS}=10V$
- Very Low On-resistance  $R_{DS(ON)}$
- Low  $C_{rss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

## 2. Applications

- PWM Application
- Load Switch
- Power Management

## 3. Symbol



Pin	Function
1	Gate
2	Drain
3	Source

#### 4. Ordering Information

Part Number	Package	Brand
KND2904A	TO-252	KIA
KNB2904A	TO-263	KIA
KNP2904A	TO-220	KIA

#### 5. Absolute maximum ratings

$T_C=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Rating		Units
		TO-252	TO-263 TO-220	
Drain-source voltage <sup>1)</sup>	$V_{DSS}$	40		V
Gate-to-Source Voltage	$V_{GSS}$	$\pm 20$		V
Continuous drain current <sup>2),3)</sup>	$T_C=25^\circ\text{C}$ $I_D$	130		A
	$T_C=100^\circ\text{C}$ $I_D$	84		A
Pulsed Drain Current at $V_{GS}=10\text{V}$ <sup>1)</sup>	$I_{DM}$	400		A
Single pulse avalanche energy <sup>2)</sup>	$E_{AS}$	250		mJ
Power dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	130	328	W
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	300		$^\circ\text{C}$
Operating junction and storage temperature range	$T_J, T_{STG}$	-55 to 150		$^\circ\text{C}$

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

#### 6. Thermal characteristics

Parameter	Symbol	Rating		Unit
		TO-252	TO-263/TO-220	
Thermal resistance junction-case	$R_{\theta JC}$	0.96	0.38	$^\circ\text{C/W}$

## 7. Electrical characteristics

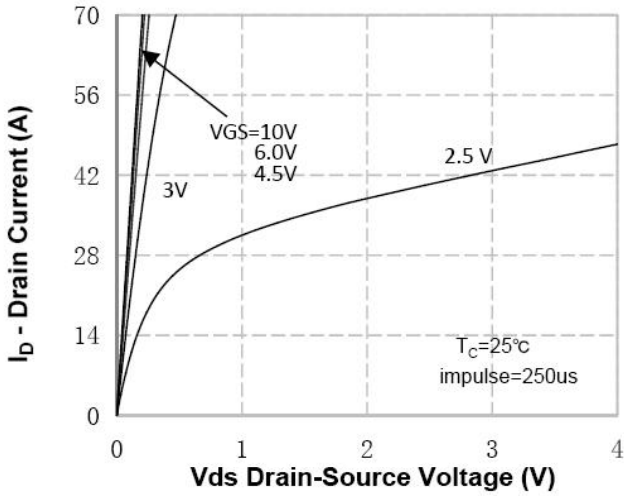
(T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	-	-	V
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-source forward leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate threshold voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0	1.5	2.3	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	2.5	3.2	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	-	3.1	4.2	mΩ
Gate Series Resistance	R <sub>G</sub>	f=1MHz	-	1.3	-	Ω
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1MHz	-	6260	-	pF
Reverse transfer capacitance	C <sub>oss</sub>		-	580	-	pF
Output capacitance	C <sub>rss</sub>		-	570	-	pF
Turn-on delay time	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V, R <sub>L</sub> =3Ω, I <sub>D</sub> =10A T <sub>J</sub> =25°C <sup>3)</sup>	-	18	-	ns
Rise time	t <sub>r</sub>		-	20	-	ns
Turn-off delay time	t <sub>d(off)</sub>		-	50	-	ns
Fall time	t <sub>f</sub>		-	16	-	ns
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =20A V <sub>GS</sub> =10V <sup>3)</sup>	-	135	-	nC
Gate-source charge	Q <sub>gs</sub>		-	30	-	nC
Gate-drain charge	Q <sub>gd</sub>		-	19	-	nC
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	—	-	-	130	A
Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>	—	-	-	400	A
Drain to Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>SD</sub> =20A, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	-	-	1.2	V

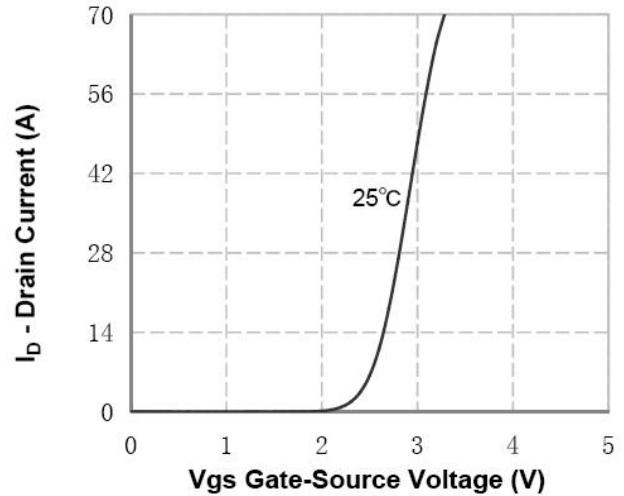
Note:

- 1). Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2). EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=20V, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH.
- 3). Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

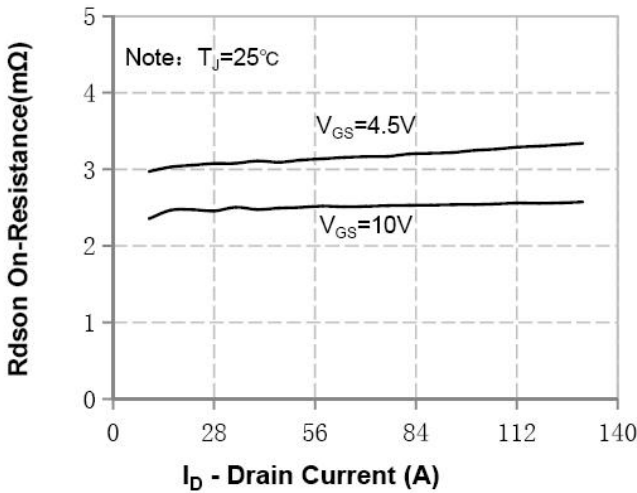
**8. Typical operating characteristics**



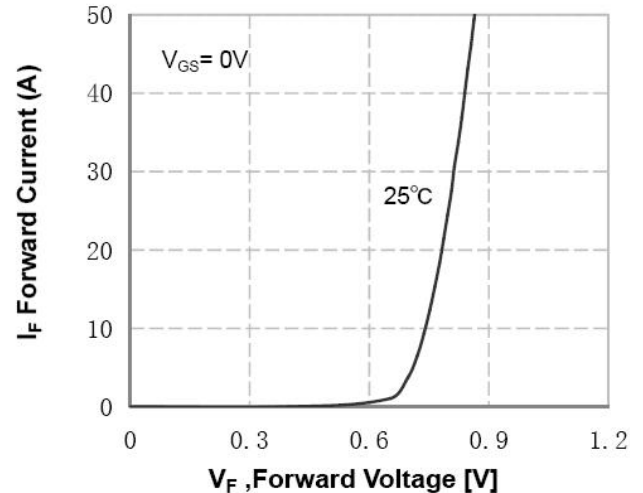
**Figure 1. On-Region Characteristics**



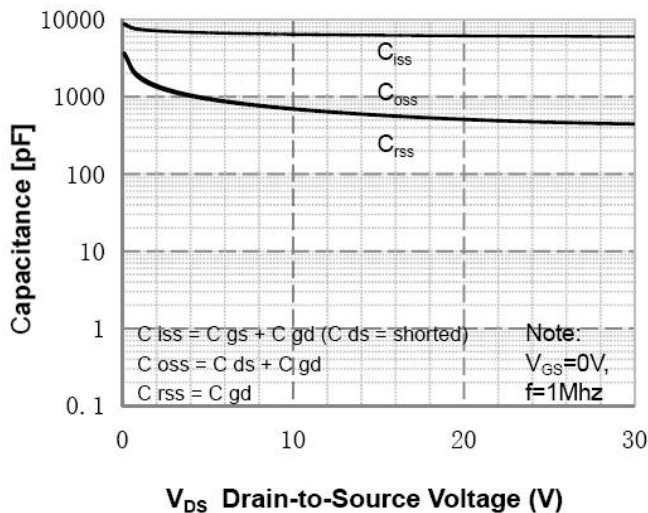
**Figure 2. Transfer Characteristics**



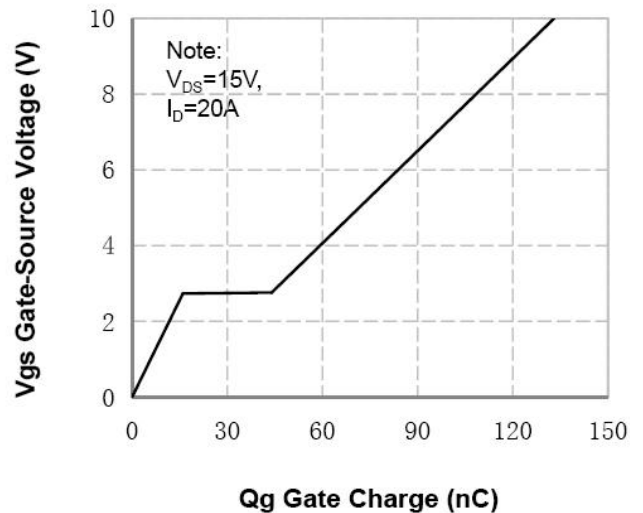
**Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage**



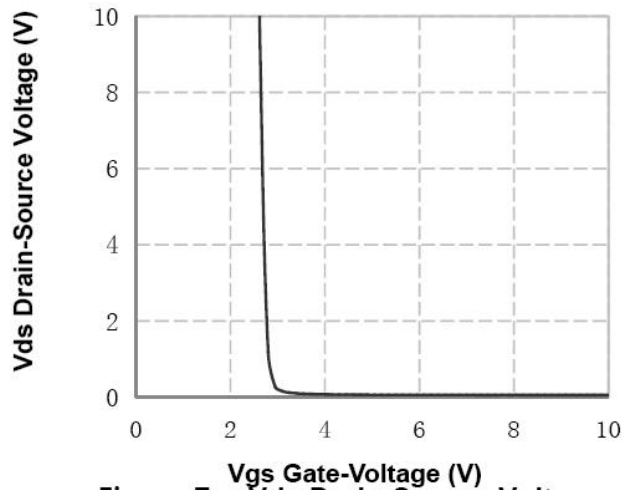
**Figure 4. Body Diode Forward Voltage Variation with Source Current**



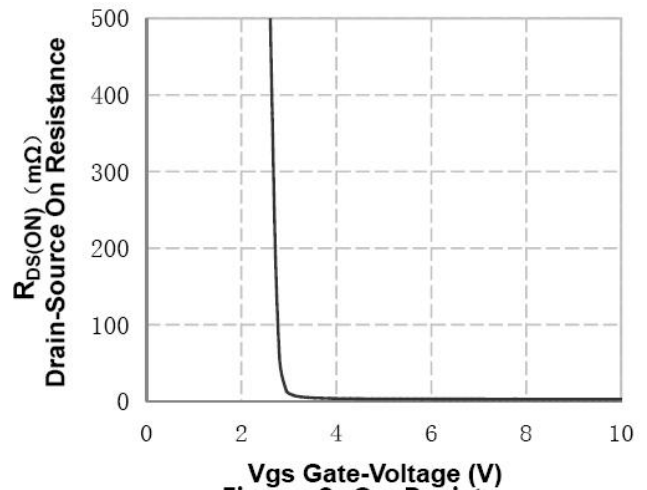
**Figure 5. Capacitance Characteristics**



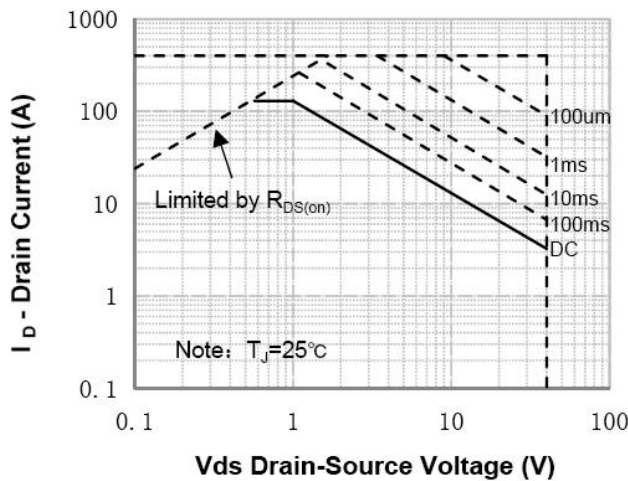
**Figure 6. Gate Charge Characteristics**



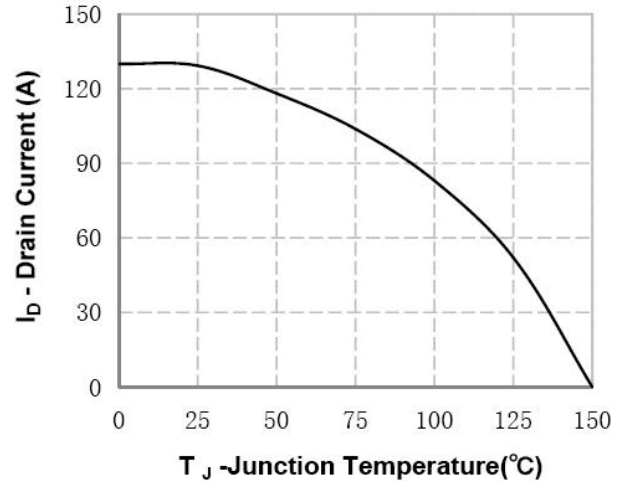
**Figure 7. Vds Drain-Source Voltage vs Gate Voltage**



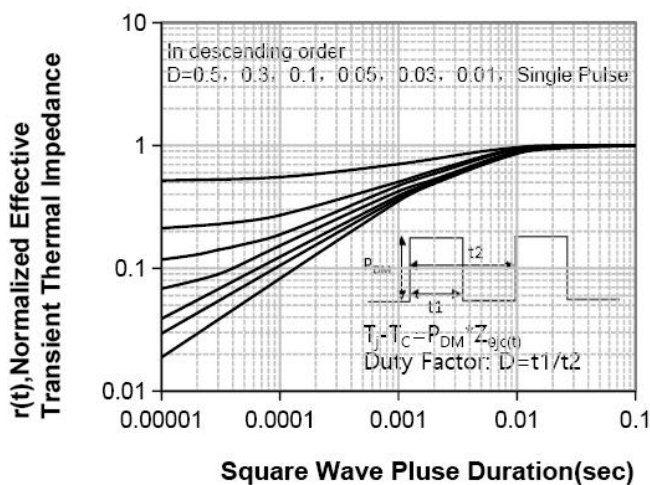
**Figure 8. On-Resistance vs Gate Voltage**



**Figure 9. Maximum Safe Operating Area**



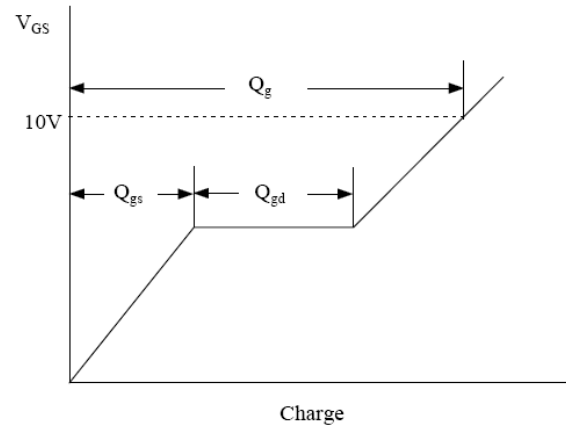
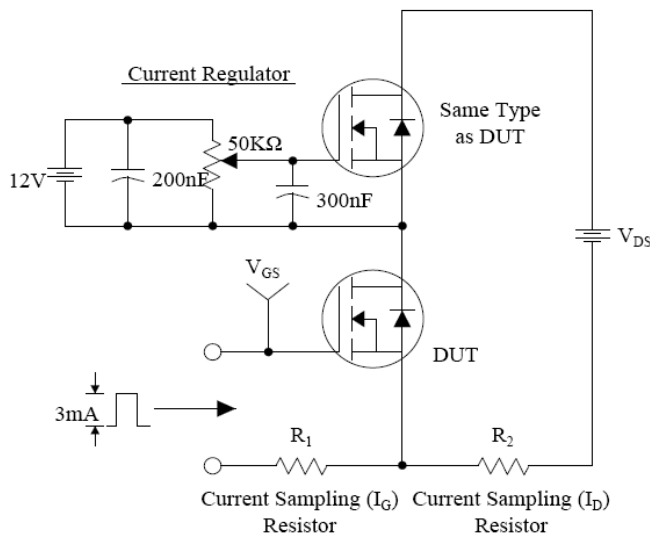
**Figure 10. Maximum Continuous Drain Current vs Temperature**



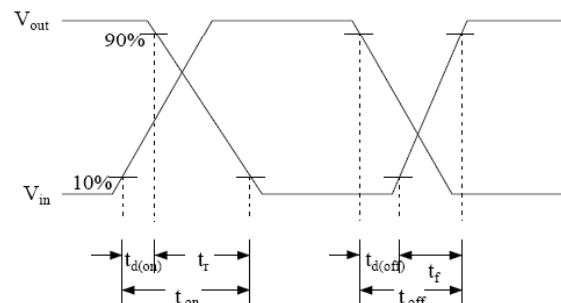
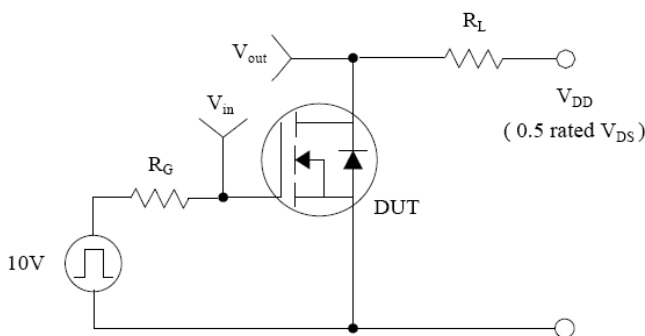
**Figure 11. Transient Thermal Response Curve**

**9. Test Circuits and Waveforms**

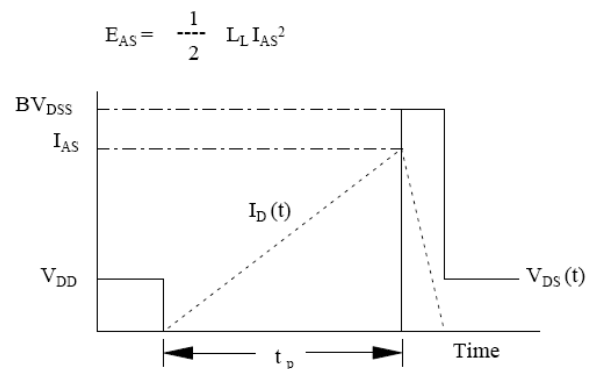
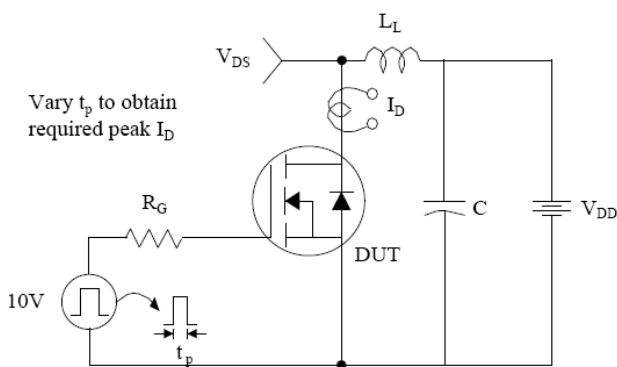
**Gate Charge Test Circuit & Waveform**



**Resistive Switching Test Circuit & Waveforms**



**Unclamped Inductive Switching Test Circuit & Waveforms**



### Peak Diode Recovery dv/dt Test Circuit & Waveforms

